SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY

Ichchhanath, Surat-395007 (Gujarat), India

REPORT SHEET SHOWING RESULT OF CANDIDATES APPEARED AT M.TECH. I SECOND SEMESTER VLSI AND EMBEDDED SYSTEMS AT REGULAR EXAM HELD DURING APRIL/MAY 2024

Semester / Student Name / Course Code / Credit / Admission Number / Reg-Credit / Earn-Credit / SGPA / CGPA	1	2	3	4	5	6	7	8	9	10	11	12	13	14	Remark
	EC614 T	EC612 T	EC616 P	EC602 T	EC636 T	EC644 T									
	3.00	**3.00**	**4.00**	**3.00**	**3.00**	**3.00**									
	REAL TIME SYSTEMS	ANALOG VLSI DESIGN	VLSI LABORATORY - II	IMAGE PROCESSING (ELECTIVE)	AD-HOC NETWORKS (ELECTIVE)	TESTING AND VERIFICATION OF VLSI CIRCUTS (ELECTIVE)									
Semester : 2 PAMULA BINDU **P23VL002** | **19.00** | **19.00** | **8.37** | **7.45**	AB	AB	AB	BB	CC	AB									
Semester / Student Name / Course Code / Credit / Admission Number / Reg-Credit / Earn-Credit / SGPA / CGPA	EC614 T	EC612 T	EC616 P	EC636 T	EC642 T	EC644 T									
	3.00	**3.00**	**4.00**	**3.00**	**3.00**	**3.00**									
	REAL TIME SYSTEMS	ANALOG VLSI DESIGN	VLSI LABORATORY - II	AD-HOC NETWORKS (ELECTIVE)	VLSI SYSTEM DESIGN (ELECTIVE)	TESTING AND VERIFICATION OF VLSI CIRCUTS (ELECTIVE)									
Semester : 2 SUMIT DUBEY **P23VL004** | **19.00** | **19.00** | **8.05** | **7.24**	AA	AB	AB	BB	DD	BB									
Semester : 2 AYUSHI JAR **P23VL005** | **19.00** | **19.00** | **8.37** | **7.97**	BB	AB	AB	BB	BC	AB									
Semester / Student Name / Course Code / Credit / Admission Number / Reg-Credit / Earn-Credit / SGPA / CGPA	EC614 T	EC612 T	EC616 P	EC642 T	EC644 T	EC622 T									
	3.00	**3.00**	**4.00**	**3.00**	**3.00**	**3.00**									
	REAL TIME SYSTEMS	ANALOG VLSI DESIGN	VLSI LABORATORY - II	VLSI SYSTEM DESIGN (ELECTIVE)	TESTING AND VERIFICATION OF VLSI CIRCUTS (ELECTIVE)	LOW POWER VLSI DESIGN (ELECTIVE)									
Semester : 2 SHREYASI MODAK **P23VL006** | **19.00** | **19.00** | **7.89** | **7.74**	BB	BB	AB	CC	AB	BC									
Semester / Student Name / Course Code / Credit / Admission Number / Reg-Credit / Earn-Credit / SGPA / CGPA	EC614 T	EC612 T	EC616 P	EC636 T	EC642 T	EC644 T									
	3.00	**3.00**	**4.00**	**3.00**	**3.00**	**3.00**									
	REAL TIME SYSTEMS	ANALOG VLSI DESIGN	VLSI LABORATORY - II	AD-HOC NETWORKS (ELECTIVE)	VLSI SYSTEM DESIGN (ELECTIVE)	TESTING AND VERIFICATION OF VLSI CIRCUTS (ELECTIVE)									
Semester : 2 RAJPUT ANIL KUMAR RAVINDRAPRASAD **P23VL007** | **19.00** | **19.00** | **8.26** | **8.08**	BB	AB	AA	CC	BC	AB									

Abbrivations : SGPA = Semester Grade Point Average, CGPA = Cumulative Grade Point Average Grade : AA-10 AB-9 BB-8 BC-7 CC-6 CD-5 DD-4 FF-0 II - INCOMPLETE NA - NOT APPEARED XX - UNSATISFATORY ATTENDANCE NP/NF-AUDIT PASS/FAIL

Date : 21-05-2024

The actual mark statement with the Academic / Examination section shall be final & binding to all

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Semester / Student Name / Course Code / Credit / Admission Number / Reg-Credit / Earn-Credit / SGPA / CGPA	1	2	3	4	5	6	7	8	9	10	11	12	13	14	Remark
	EC614 T	EC612 T	EC616 P	EC642 T	EC644 T	EC622 T									
	3.00	**3.00**	**4.00**	**3.00**	**3.00**	**3.00**									
	REAL TIME SYSTEMS	ANALOG VLSI DESIGN	VLSI LABORATORY - II	VLSI SYSTEM DESIGN (ELECTIVE)	TESTING AND VERIFICATION OF VLSI CIRCUTS (ELECTIVE)	LOW POWER VLSI DESIGN (ELECTIVE)									
Semester : 2 LOHAR AJAY ADHIK VIDYA **P23VL008** | **19.00** | **19.00** | **8.74** | **9.13**	BC	AA	AA	AB	BB	BB									
Semester / Student Name / Course Code / Credit / Admission Number / Reg-Credit / Earn-Credit / SGPA / CGPA	EC614 T	EC612 T	EC616 P	EC636 T	EC642 T	EC644 T									
	3.00	**3.00**	**4.00**	**3.00**	**3.00**	**3.00**									
	REAL TIME SYSTEMS	ANALOG VLSI DESIGN	VLSI LABORATORY - II	AD-HOC NETWORKS (ELECTIVE)	VLSI SYSTEM DESIGN (ELECTIVE)	TESTING AND VERIFICATION OF VLSI CIRCUTS (ELECTIVE)									
Semester : 2 SANAKA RAKESH **P23VL009** | **19.00** | **19.00** | **7.42** | **7.45**	BC	AB	AB	CD	BC	BC									
Semester : 2 SANJAY SINGH YADAV **P23VL011** | **19.00** | **19.00** | **7.26** | **6.89**	BB	BB	AB	CC	CD	BC									
Semester : 2 YASH GUPTA **P23VL012** | **19.00** | **19.00** | **8.05** | **7.87**	BC	AB	AB	BC	BB	BB									
Semester : 2 KOPPULA BHANU VENKATA PRAKASH REDDY **P23VL013** | **19.00** | **19.00** | **8.21** | **7.55**	AA	AB	AB	BC	CD	AB									
Semester : 2 BRAJIT PAUL **P23VL014** | **19.00** | **19.00** | **8.42** | **8.13**	BB	AB	AA	BC	BB	BB									
Semester / Student Name / Course Code / Credit / Admission Number / Reg-Credit / Earn-Credit / SGPA / CGPA	EC614 T	EC612 T	EC616 P	EC642 T	EC644 T	EC622 T									
	3.00	**3.00**	**4.00**	**3.00**	**3.00**	**3.00**									
	REAL TIME SYSTEMS	ANALOG VLSI DESIGN	VLSI LABORATORY - II	VLSI SYSTEM DESIGN (ELECTIVE)	TESTING AND VERIFICATION OF VLSI CIRCUTS (ELECTIVE)	LOW POWER VLSI DESIGN (ELECTIVE)									
Semester : 2 RISHABH JANGID **P23VL015** | **19.00** | **19.00** | **8.21** | **8.21**	BB	BB	AB	BC	AB	BB									
Semester : 2 SHEETAL VERMA **P23VL016** | **19.00** | **19.00** | **8.53** | **8.13**	AA	AA	AB	CC	AB	BC									

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	1	2	3	4	5	6	7	8	9	10	11	12	13	14	Remark
Semester Student Name *Course Code*	EC614 T	EC612 T	EC616 P	EC636 T	EC642 T	EC644 T									
Credit	**3.00**	**3.00**	**4.00**	**3.00**	**3.00**	**3.00**									
Admission Number Reg-Credit Earn-Credit SGPA CGPA	REAL TIME SYSTEMS	ANALOG VLSI DESIGN	VLSI LABORATORY - II	AD-HOC NETWORKS (ELECTIVE)	VLSI SYSTEM DESIGN (ELECTIVE)	TESTING AND VERIFICATION OF VLSI CIRCUTS (ELECTIVE)									
Semester : 2 SUDHANSHU KUMAR YADAV **P23VL017** | **19.00** | **19.00** | **8.37** | **7.82**	AB	AB	AB	BB	BC	BB									
Semester : 2 KADALI SRAMIK **P23VL018** | **19.00** | **19.00** | **7.74** | **7.39**	AB	AB	AB	CD	CC	BB									
Semester : 2 PRATYUSH ANAND **P23VL019** | **19.00** | **19.00** | **7.42** | **7.50**	BB	AB	AB	CD	CD	BB									
Semester : 2 ABHISHEK SINGH **P23VL021** | **19.00** | **19.00** | **7.58** | **7.08**	AB	AB	AB	CC	DD	BB									
Semester Student Name *Course Code*	EC614 T	EC612 T	EC616 P	EC642 T	EC644 T	EC622 T									
Credit	**3.00**	**3.00**	**4.00**	**3.00**	**3.00**	**3.00**									
Admission Number Reg-Credit Earn-Credit SGPA CGPA	REAL TIME SYSTEMS	ANALOG VLSI DESIGN	VLSI LABORATORY - II	VLSI SYSTEM DESIGN (ELECTIVE)	TESTING AND VERIFICATION OF VLSI CIRCUTS (ELECTIVE)	LOW POWER VLSI DESIGN (ELECTIVE)									
Semester : 2 POOJA GUPTA **P23VL022** | **19.00** | **19.00** | **8.21** | **8.53**	AB	AB	AB	BB	BC	BC									
Semester : 2 CHERUKU RAHUL BHASKAR **P23VL023** | **19.00** | **19.00** | **9.53** | **9.37**	AB	AA	AA	AB	AA	AB									
Semester Student Name *Course Code*	EC614 T	EC612 T	EC616 P	EC636 T	EC642 T	EC644 T									
Credit	**3.00**	**3.00**	**4.00**	**3.00**	**3.00**	**3.00**									
Admission Number Reg-Credit Earn-Credit SGPA CGPA	REAL TIME SYSTEMS	ANALOG VLSI DESIGN	VLSI LABORATORY - II	AD-HOC NETWORKS (ELECTIVE)	VLSI SYSTEM DESIGN (ELECTIVE)	TESTING AND VERIFICATION OF VLSI CIRCUTS (ELECTIVE)									
Semester : 2 RAJNEESH KUMAR PATEL **P23VL024** | **19.00** | **19.00** | **7.74** | **7.29**	AB	BB	AB	CC	BC	BC									
Semester Student Name *Course Code*	EC614 T	EC612 T	EC616 P	EC602 T	EC636 T	EC644 T									
Credit	**3.00**	**3.00**	**4.00**	**3.00**	**3.00**	**3.00**									
Admission Number Reg-Credit Earn-Credit SGPA CGPA	REAL TIME SYSTEMS	ANALOG VLSI DESIGN	VLSI LABORATORY - II	IMAGE PROCESSING (ELECTIVE)	AD-HOC NETWORKS (ELECTIVE)	TESTING AND VERIFICATION OF VLSI CIRCUTS (ELECTIVE)									
Semester : 2 PARMAR HARSHKUMAR DOLATBHAI **P23VL025** | **19.00** | **19.00** | **7.89** | **7.00**	BB	BB	AB	BC	BC	BB									

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	1	2	3	4	5	6	7	8	9	10	11	12	13	14	Remark

Total Student Count : 21